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Substitute for form 1449A/PTO				Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Application Number	10/759483
				Filing Date	1/16/2004
				First Named Inventor	Rodney E. Hooker
				Group Art Unit	2188
				Examiner Name	Mano Padmadabahn
Sheet	1	of	1	Attorney Docket Number	CNTR.2232

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number Number-Kind Code ^{2,3} (Inventor)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
RD		5930820	07-27-1999	Thomas W. Lynch	
RD		6161661	11-21-2000	Adams et al.	
RD		20040186959	09-23-2004	Takuji Kawamoto	
RD		6415380	07-02-2002	Toshinori Sato	

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document Country Code ³ - Number ⁴ - Kind Code ⁵ (Inventor)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
RD	AA	GB2307319	05-21-1997	Han Heung Kim		

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	BA	"Dual On-Chip Instruction Cache Organization in High Speed Processors." IBM Technical Disclosure Bulletin. IBM Corp. New York, US. Volume 37, No. 12, December 1994, pages 213-214.	

Examiner Signature	<i>John A. Over</i>	Date Considered	3/3/06
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U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
RD		2002144061	10-03-2002	Feanes et al.	
RD		2004133886	07-08-2004	Youfang Wu	

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document Country Code ² - Number ³ - Kind Code ⁴ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁵

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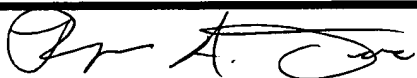
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				First Named Inventor	Rodney E. Hooker
				Group Art Unit	
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		Number	Kind Code ²			
RD	AA	5043870		Ditzel et al	08/27/1991	
RD	AB	5953741		Evoy et al	09/14/1999	
RD	AC	6009499		Sailendra Koppala	12/28/1999	
RD	AD	6532531		O'Connor et al	03/11/2003	

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OTHER PRIOR ART-NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
RD	AA	(in the file SCreport.pdf): Implementing a Stack Cache; Hensath, Morton, Sjodin; obtained from http://www.owinnet.rice.edu/~elec525/projects/SCreport.pdf ; accessed on 8-13-2003.	
RD	AB	(in the file JavaStack Cache.doc); Design Issues - Stack Cache; obtained from http://murray.newcastle.edu.au/users/students/1999/c9510422/design1.html ; http://murray.newcastle.edu.au/users/students/1999/c9510422/design2.html ; http://murray.newcastle.edu.au/users/students/1999/c9510422/design3.html ; http://murray.newcastle.edu.au/users/students/1999/c9510422/design4.html ; http://murray.newcastle.edu.au/users/students/1999/c9510422/architecture.html ; accessed on 12-02-2003.	
RD	AC	(in the fine AMD Hammer micro architecture.pdf): AMD's Hammer micro architecture; http://chip-architect.com/news/hammer.jpg ; accessed on 08-29-2002.	

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